

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

Method of Manufacturing a Semiconductor Device and a Method
for Fixing the Semiconductor Device Using Substrate Jig

Application Number :

Confirmation Number:

First Named Applicant: Kazuo TESHIROGI

Attorney Docket Number: 020334A

Art Unit:

Examiner:

Search string: (6037026 or 5631191 or 4016829 or 5458755).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6037026	2000-03-14	Iwamoto			
	2	5631191	1997-05-20	Durand et al.			
	3	4016829	1977-04-12	Ito et al.			
	4	5458755	1995-10-17	Fujiyama et al.			

Signature

Examiner Name	Date